

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD FOR FABRICATING INTEGRATED CIRCUITS HAVING BOTH HIGH VOLTAGE AND LOW VOLTAGE DEVICES				
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Jung-Ching CHEN</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: NAUP0575USA</p>					
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td></tr><tr><td>Winston Hsu Registered Number: 41,526</td><td>Winston Hsu</td></tr></table>		Submitted By:	Elec. Sign.	Winston Hsu Registered Number: 41,526	Winston Hsu
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Documents being submitted:	Files
us-power-of-attorney-grant	NAUP0575_0_A1-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-assignment	NAUP0575_0_A1-usassn.xml us-assignment.xsl us-assignment.dtd NAUP0575_0_A1ASS1.tif NAUP0575_0_A1ASS2.tif
us-request	NAUP0575_0_A1-usrequ.xml us-request.dtd us-request.xsl
us-declaration	NAUP0575_0_A1-usdecl.xml us-declaration.dtd us-declaration.xsl
us-fee-sheet	NAUP0575_0_A1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	NAUP0575-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Naup0575usa-01.tif Naup0575usa-02.tif Naup0575usa-03.tif Naup0575usa-04.tif

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us-declaration	NAUP0575_0_A1DEC1.tif
us-declaration	NAUP0575_0_A1DEC2.tif
Comments	